I'm Receipt

PATENT APPLICATION 2826 Docket No: 14321.67

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)
	Nobuhiro Nunoya et al.))
Serial No.:	10/527,355) Art Uni
Filed:	March 7, 2005) 2811
Confirmation No.:	4938))
For:	OPTICAL SEMICONDUCTOR DEVICE AND OPTICAL SEMICONDUCTOR INTEGRATED CIRCUIT	<i>)</i>))

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that the following documents are being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450, on the 5th day of January 2006.

- Petition to Correct Filing Receipt (2 pages)
- Copy of Filing Receipt (2 pages)
- Postcard

Respectfully submitted,

DANA L. TANGREN

Attorney for Applicant Registration No. 37,246

Customer No. 022913

Telephone No. 801.533.9800

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PATENT APPLICATION

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PETITION FOR CORRECTION OF FILING RECEIPT

Commissioner for Patents PO Box 1450 Alexandria, Virginia 22313-1450

Dear Sir:

Attached is a copy of the official Filing Receipt received from the United States Patent and Trademark Office in the above-identified application for which issuance of a corrected Filing Receipt is respectfully requested.

Please amend the Filing Receipt under the section Title as follows:

Optical semiconductor device and optical semiconductor in tegrated integrated circuit

The correction is not due to any error by applicant, and accordingly it is respectfully submitted that no fee is due relative thereto. A marked copy of the filing receipt showing the request change is attached.

Dated this 5th day of January 2006.

Respectfully submitted,

DANA L. TANGREN Attorney for Applicant Registration No. 37,246

Customer No. 022913

Telephone No. 801.533.9800

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UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P.O. BOX 1450 Alexandria, Virginia 22313-1450

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/527,355	03/07/2005	2811	2400	14321.67	25	19	8

CONFIRMATION NO. 4938

022913 WORKMAN NYDEGGER (F/K/A WORKMAN NYDEGGER & SEELEY)

60 EAST SOUTH TEMPLE 1000 EAGLE GATE TOWER SALT LAKE CITY, UT 84111 ELEY)

FILING RECEIPT

OC000000017359318

Date Mailed: 11/16/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Nobuhiro Nunoya, Kanagawa, JAPAN; Yasuo Shibata, Tokyo, JAPAN; Naoki Fujiwara, Tokyo, JAPAN; Nobuhiro Kikuchi, Kanagawa, JAPAN; Yuichi Tomori, Kanagawa, JAPAN;

Power of Attorney: The patent pr

ers associated with Customer Number 022913.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/04517 03/30/2004

Foreign Applications

JAPAN 2003-094696 03/31/2003 JAPAN 2003-400156 11/28/2003 JAPAN 2003-412062 12/10/2003

Projected Publication Date: To Be Determined - pending completion of Security Review

Non-Publication Request: No

Early Publication Request: No

Title

Optical semiconductor device and optical semiconductor in-tegrated circuit

Preliminary Class

257

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